

ABSTRACT

An optical receiver uses a clock data recovery block to improve the jitter and the power consumption of the optical receiver. The optical receiver includes a photodetector for receiving an optical signal and generating a corresponding current signal, a gain stage
5 coupled to the photodetector for receiving the corresponding current signal and converting it to a corresponding voltage signal, and a clock data recovery circuit coupled to the gain stage for receiving the corresponding voltage signal, extracting clock information from the corresponding voltage signal, and regenerating the corresponding voltage signal to reduce jitter.

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